



**产品规格手册**  
PRODUCT SPECIFICATION MANUAL

An abstract background graphic featuring a complex network of interconnected nodes. The nodes are colored in shades of blue, teal, purple, and pink, forming a triangular mesh pattern that serves as a backdrop for the title text.

## KM8205A

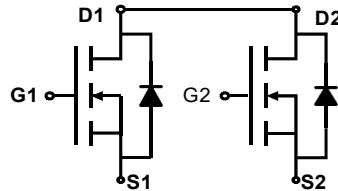
**Dual N-Channel Enhancement Mode Power MOSFET  
TSSOP8/20V/6.5A**





## Description

The KM8205A uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.



Schematic diagram

## General Features

- $V_{DS} = 20V, I_D = 6.5A$
- $R_{DS(ON)} < 21m\Omega @ V_{GS}=4.5V$
- $R_{DS(ON)} < 27m\Omega @ V_{GS}=2.5V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

## Application

- Battery protection
- Load switch
- Power management



Marking and pin Assignment



TSSOP-8 top view

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Product	Package	Marking	Packing	Min Unit Quantity
KM8205A	TSSOP8	8205A	5000PCS/Reel	5000PCS

## Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Drain Current-Continuous	$I_D$	6.5	A
Drain Current-Pulsed (Note 1)	$I_{DM}$	25	A
Maximum Power Dissipation	$P_D$	1.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

## Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	83	°C/W
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**Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)**

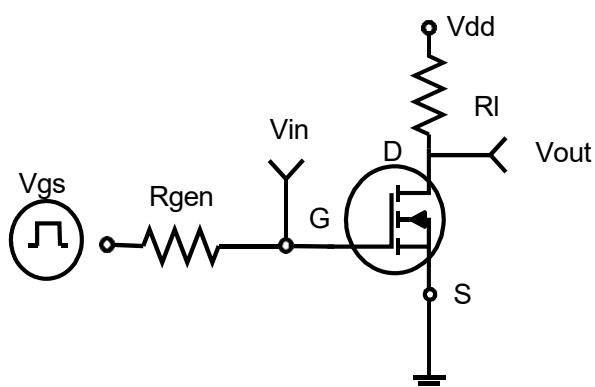
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	20	21	-	V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=19.5\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Body Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 10\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	0.5	0.7	1.2	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=4.5\text{A}$	-	21	27	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=2.5\text{V}, \text{I}_D=3.5\text{A}$	-	27	37	$\text{m}\Omega$
Forward Transconductance	$\text{g}_{\text{FS}}$	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=4.5\text{A}$	-	10	-	S
<b>Dynamic Characteristics (Note 4)</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=8\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $\text{f}=1.0\text{MHz}$	-	600	-	PF
Output Capacitance	$\text{C}_{\text{oss}}$		-	330	-	PF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		-	140	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=10\text{V}, \text{I}_D=1\text{A}$ $\text{V}_{\text{GS}}=4.5\text{V}, \text{R}_{\text{GEN}}=6\Omega$	-	10	20	nS
Turn-on Rise Time	$t_r$		-	11	25	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	35	70	nS
Turn-Off Fall Time	$t_f$		-	30	60	nS
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=6\text{A},$ $\text{V}_{\text{GS}}=4.5\text{V}$	-	10	15	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		-	2.3	-	nC
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		-	1.5	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=1.7\text{A}$	-	0.75	1.2	V
Diode Forward Current (Note 2)	$\text{I}_s$		-	-	1.7	A

**Notes:**

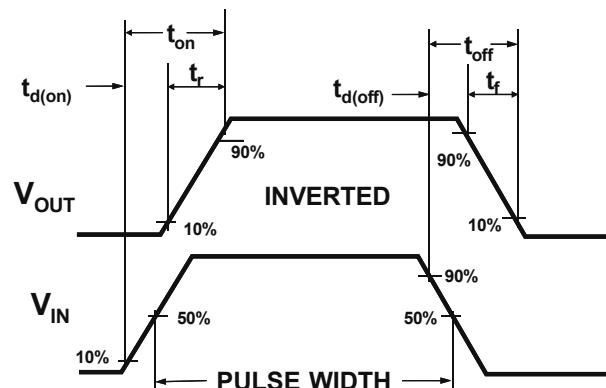
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production.

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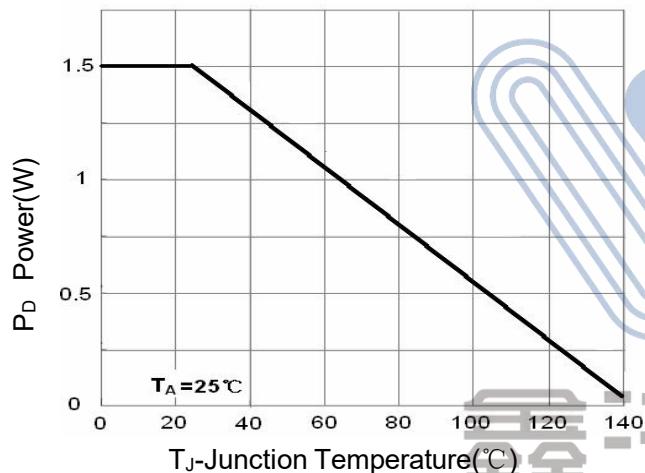
## Typical Electrical and Thermal Characteristics



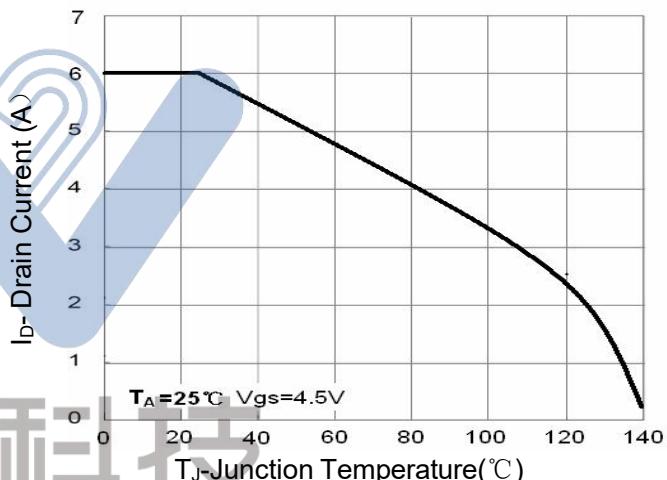
**Figure 1:Switching Test Circuit**



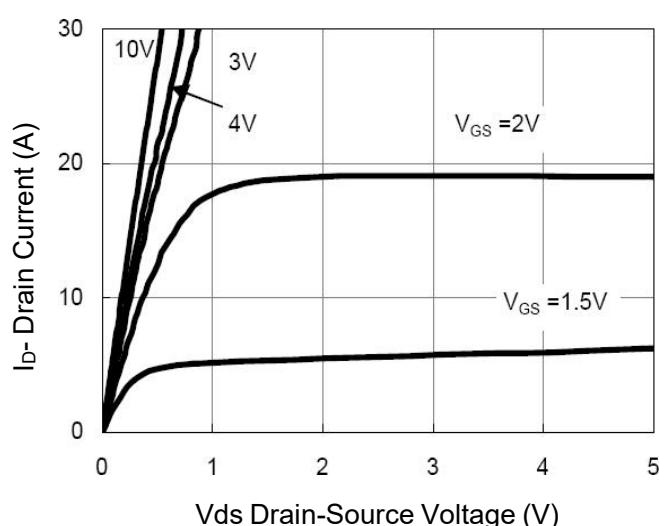
**Figure 2:Switching Waveforms**



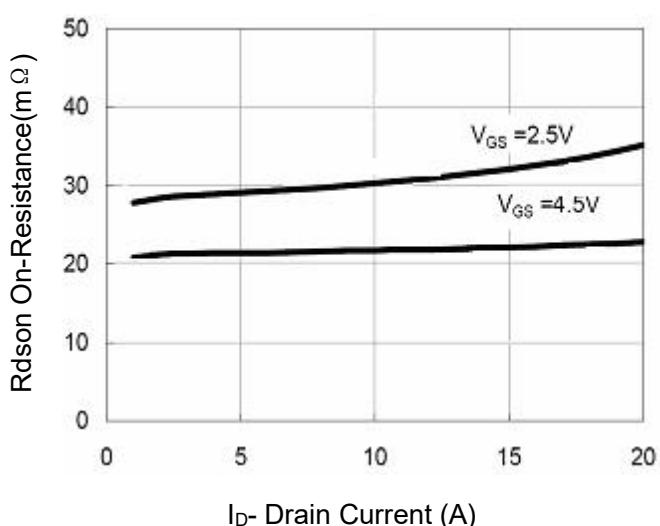
**Figure 3 Power Dissipation**



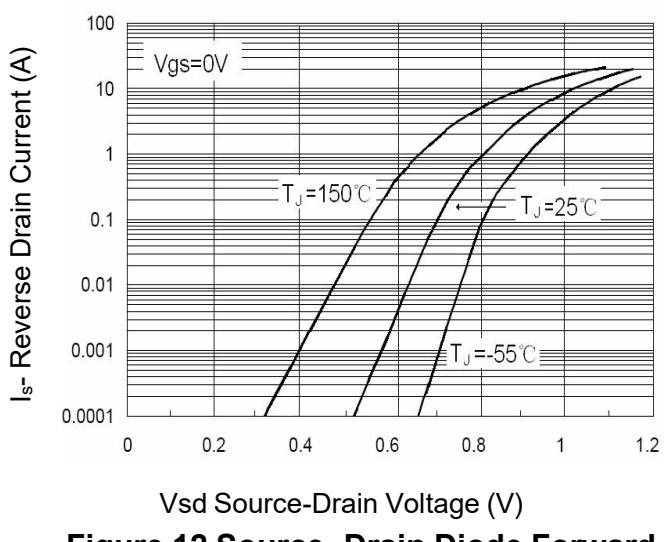
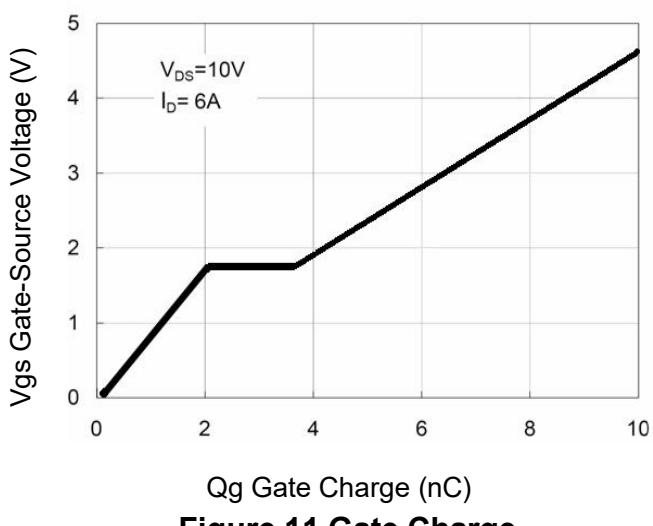
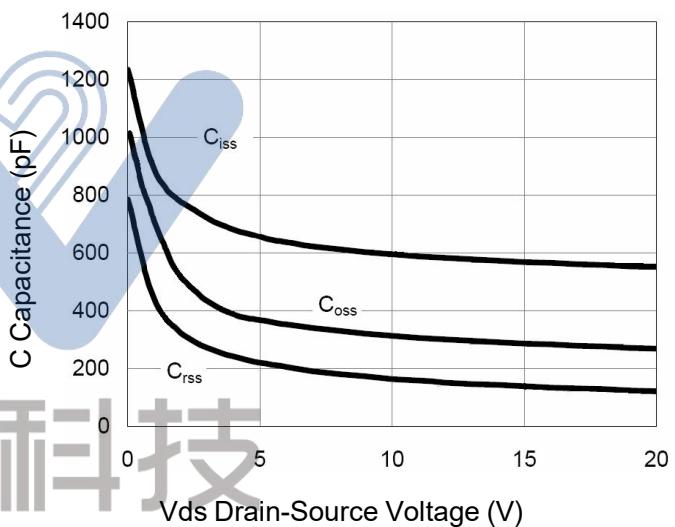
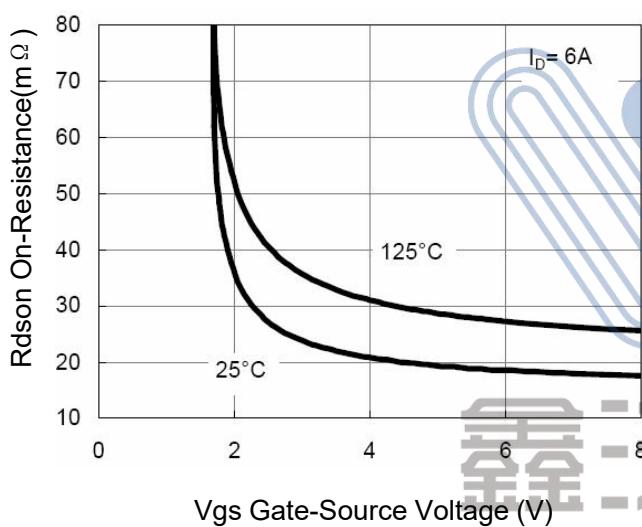
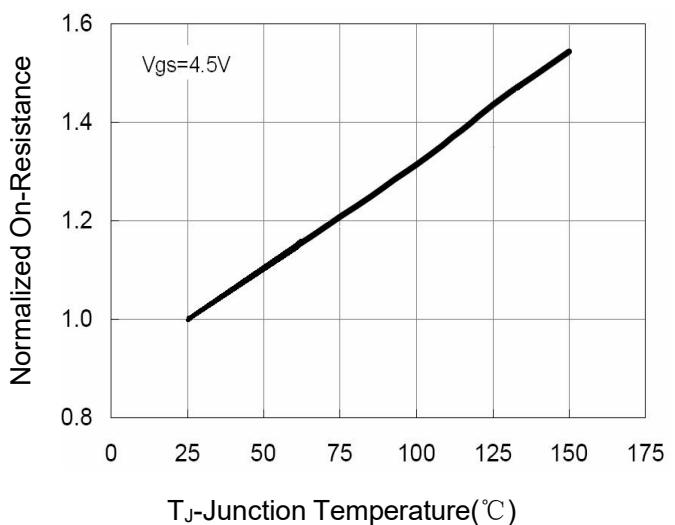
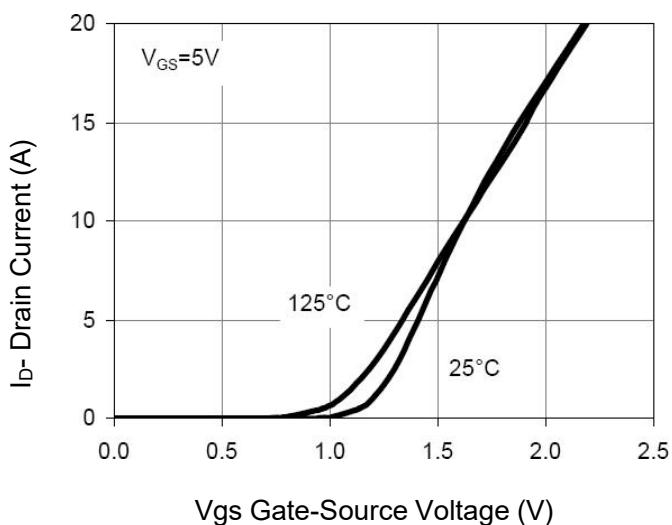
**Figure 4 Drain Current**



**Figure 5 Output Characteristics**



**Figure 6 Drain-Source On-Resistance**



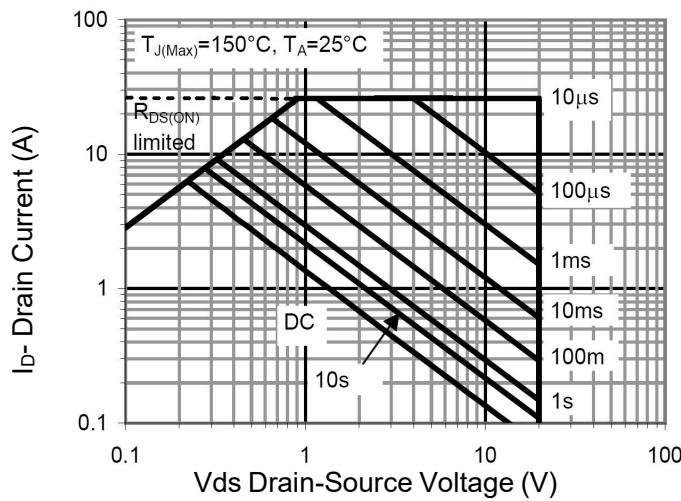
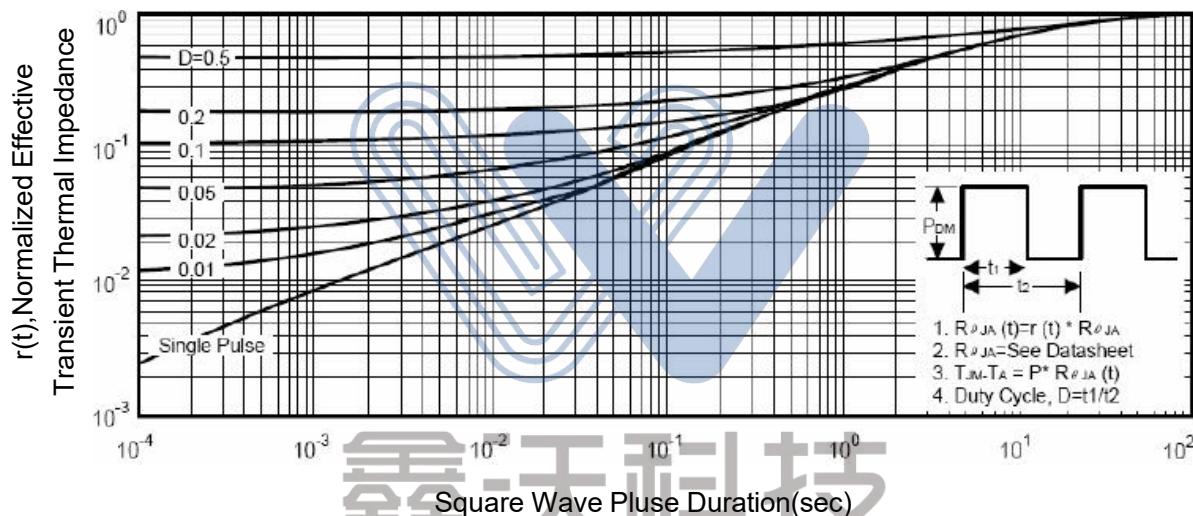
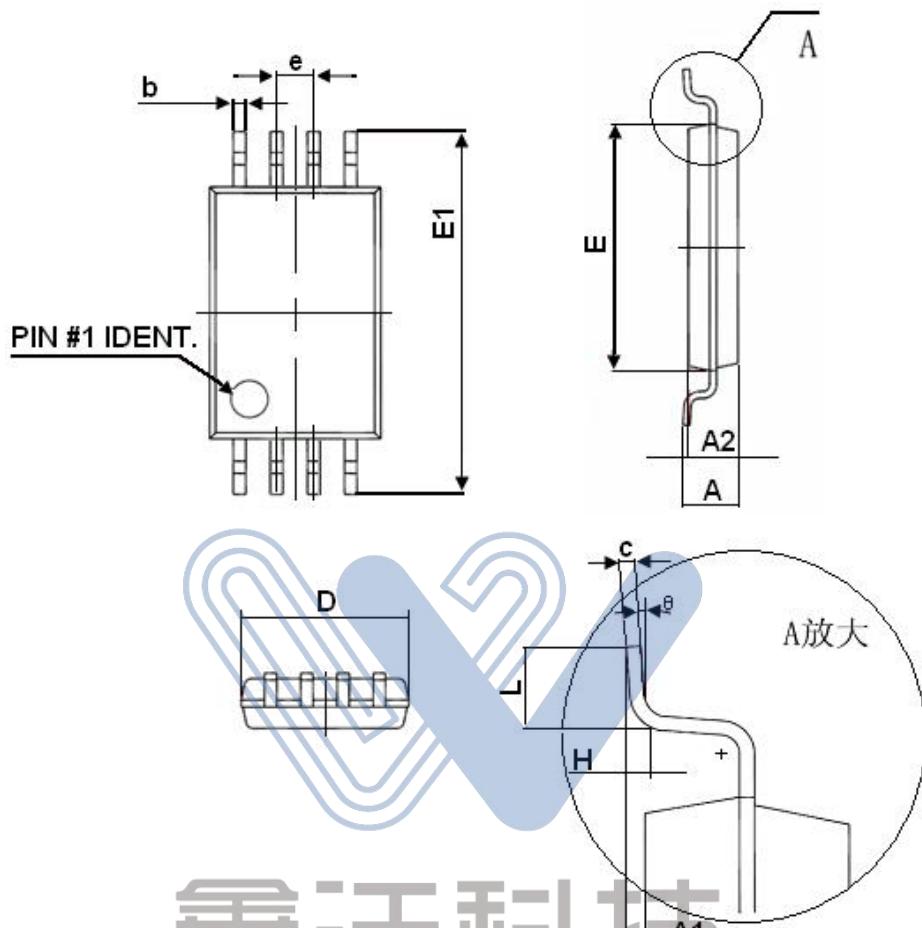


Figure 13 Safe Operation Area

Figure 14 Normalized Maximum Transient Thermal Impedance  
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## Tssop-8 Package Information



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Symbol	Dimensions In Millimeters	
	Min	Max
D	2.900	3.100
E	4.300	4.500
b	0.190	0.300
c	0.090	0.200
E1	6.250	6.550
A		1.100
A2	0.800	1.000
A1	0.020	0.150
e	0.65(BSC)	
L	0.500	0.700
H	0.25(TYP)	
Θ	1°	7°